



SAN FRANCISCO, CA • JUNE 7 - 11, 2015 • DAC.COM

CALL FOR CONTRIBUTIONS

DAC OFFERS THE BEST-IN-CLASS TECHNICAL PROGRAM DEVOTED TO THE ADVANCEMENT OF ELECTRONIC DESIGN AND DESIGN AUTOMATION.

DAC 2015 is seeking submissions that deal with design technologies and algorithms, addressing all aspects of electronic design across several submission categories.



TOPIC AREAS:

In addition to well established Electronic Design (EDA) and Embedded Systems and Software (ESS) tracks, additional tracks for 2015 include Automotive, Security, IP and Design. Visit DAC.com for more submission information on all tracks.



TOP LEVEL SUBMISSION CATEGORIES FOR EDA MANUSCRIPTS

Authors of research manuscripts are required to specify a category from the list below. Authors of submissions that cover cross-cutting topics (e.g. low-power, multi-core architecture, 3-D, etc.) should select a category that is closest to the essential contribution of the submission. Authors may choose a second submission category to accommodate cross-cutting contributions. Your selection should be based on the consideration of which expertise group of people would better review your submissions.

- System-on-chip design and HW/SW codesign
- On-Chip Communication and Networks-on-Chip
- Cross-Layer Power Analysis and Low-Power Design
- Verification
- RTL/Logic Level and FPGA Synthesis, Design and Testing
- Analog Design, Simulation and Testing
- Physical Design and Design Closure
- Physical Verification, Lithography and DFM
- Digital Design, Simulation and Testing
- New and Emerging Design Technologies



ABSTRACT DEADLINE:
NOVEMBER 21, 2014

MANUSCRIPT DEADLINE:
DECEMBER 2, 2014



TOP LEVEL SUBMISSION CATEGORIES FOR ESS MANUSCRIPTS

Authors of research manuscripts are required to specify a category from the list below. Authors of submissions that cover cross-cutting topics (e.g. low-power, multi-core architecture, 3-D, etc.) should select a category that is closest to the essential contribution of the submission. Authors may choose a second submission category to accommodate cross-cutting contributions. Your selection should be based on the consideration of which expertise group of people would better review your submissions.

- Embedded Software
- Embedded System Design
- Embedded Storage and I/O Systems
- Embedded System Design Methodologies

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PANEL PROPOSALS

Submission Deadline: 5:00pm MT, (-07:00 GMT) [OCTOBER 15, 2014](#)

Technical panels are organized as plenary sessions within the technical conference program. The goal is to organize panels of broad interest that attract a large audience. Panel topics should be interesting, timely, informative, and enlightening.



SPECIAL SESSION PROPOSALS

Submission Deadline: 5:00pm MT, (-07:00 GMT) [NOVEMBER 13, 2014](#)

A Special Session is devoted to one of the following topics: Traditional Core EDA, Embedded Systems and Software (ESS), Automotive, Security or a topic of future interest.



TUTORIAL PROPOSALS

Submission Deadline: 5:00pm MT, (-07:00 GMT) [NOVEMBER 13, 2014](#)

DAC is looking for timely, tangible, hands-on topics that provide immediate value for the attendee.



WORKSHOP PROPOSALS

Submission Deadline: 5:00pm MT, (-07:00 GMT) [NOVEMBER 13, 2014](#)

DAC invites you to submit a workshop on emerging topics related to design, design methodologies, and design automation.



WORK-IN-PROGRESS (WIP)

Abstract Submission Deadline: 5:00pm MT, (-07:00 GMT) [NOVEMBER 21, 2014](#)

Manuscript Submission Deadline: 5:00pm MT, (-07:00 GMT) [DECEMBER 2, 2014](#)

DAC Work-in-Progress (WIP) aims to provide authors an opportunity for early feedback on current work and preliminary results. The authors are expected to present a poster at DAC. A 100-word summary abstract will be published on the DAC website, but the submission will not be included in the official DAC proceedings.



RESEARCH MANUSCRIPTS FOR EDA, ESS, AUTOMOTIVE AND SECURITY

Abstract Submission Deadline: 5:00pm MT, (-07:00 GMT) [NOVEMBER 21, 2014](#)

Manuscript Submission Deadline: 5:00pm MT, (-07:00 GMT) [DECEMBER 2, 2014](#)



DESIGNER TRACK SUBMISSIONS

Submission Deadline: 5:00pm MT, (-07:00 GMT) [JANUARY 20, 2015](#)

DAC Designer Track brings together IC designers and embedded software developers from across the globe.



IP TRACK SUBMISSIONS

Submission Deadline: 5:00pm MT, (-07:00 GMT) [JANUARY 20, 2015](#)

The DAC IP Track brings together Intellectual Property (IP) core designers, users, and IP ecosystem providers from across the globe including leading companies such as ARM, IBM, Imagination, Intel, Qualcomm, Samsung, Synopsys, TI, Toshiba, and others who will present their products and design experiences on effective design flows, methods, and tool usage.

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