

## **38th DAC (2001) Best Paper Award**

This year, awards are made for the best papers in four categories. Winners were determined from detailed reviews of the accepted papers in the technical sessions. Each award is accompanied by a plaque. The awards are given by ACM/SIGDA (Special Interest Group on Design Automation), IEEE/CAS (Institute of Electrical and Electronics Engineers/Circuits and Systems Society) and EDA Consortium (Electronic Design Automation Consortium).

### **DESIGN TOOLS**

#### **Paper 13.1: A Practical Methodology for Early Buffer and Wire Resource Allocation**

Author: Charles J. Alpert Jiang Hu, Paul G. Villarrubia

Affiliation: *IBM Austin Research Lab., Austin, TX IBM Corp., Austin, TX*

Author: Sachin S. Sapatnekar

Affiliation: *Univ. of Minnesota, Minneapolis, MN*

### **DESIGN METHODOLOGY**

#### **Paper 20.2: Improving Bus Test via IDDT Testing and Boundary Scan**

Authors: Chris Papachristou, Massood Tabib-Azar Shiyu Yang

Affiliation: *Case Western Reserve Univ., Cleveland, OH Intel Corp., Hillsboro, OR*

#### **Paper 47.2: Analysis of On-Chip Inductance Effects using a Novel Performance Optimization Methodology for Distributed RLC Interconnects**

Author: Kaustav Banerjee Amit Mehrotra

Affiliation: *Stanford Univ., Stanford, CA Univ. of Illinois, Urbana, IL*

### **EMBEDDED SYSTEMS**

#### **Paper 49.1: Energy Efficient Fixed-Priority Scheduling for Real-Time Systems on Variable Voltage Processors**

Authors: Gang Quan, Sharon Hu

Affiliation: *Univ. of Notre Dame, Notre Dame, IN*